

Serial Number: 09/681,077

IN THE CLAIMS

1. (Original) A method of operating a digital system controlled by operation codes and operable in a plurality of operational modes, said method comprising steps of
processing an application program to insert execution bits in operational codes
preceding instructions which are not used in ones of said plurality of modes,
evaluating each operational code prior to decoding of said each operational code,
skipping an operational code responsive to detecting a particular state of an
execution bit in a preceding instruction, and
decoding remaining operational codes.
2. (Original) A method as recited in claim 1, wherein said processing is performed in response to a criterion representing one of an operating mode, a function and a peripheral device connected to said digital system.
3. (Original) A method as recited in claim 1, wherein said skipping step skips a single operational code following said preceding instruction.
4. (Original) A method as recited in claim 1, wherein said skipping step skips all operations between said preceding instruction and another instruction having an execution bit having said particular state.

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5. (Original) A method as recited in claim 4, wherein said skipping step is performed by toggling a bit in a register upon detection of an activation bit in said particular state and iteratively comparing execution bits of instructions with said bit in said register.

6. (Original) A method as recited in claim 1, wherein a plurality of execution bits are provided in each instruction by said processing step and said skipping step is performed in accordance with a number represented by said plurality of execution bits.

7. (Original) A method as recited in claim 2, wherein said skipping step skips a single operational code following said preceding instruction.

8. (Original) A method as recited in claim 2, wherein said skipping step skips all operations between said preceding instruction and another instruction having an execution bit having said particular state.

9. (Original) A method as recited in claim 8, wherein said skipping step is performed by toggling a bit in a register upon detection of an activation bit in said particular state and iteratively comparing execution bits of instructions with said bit in said register.

10. (Original) A method as recited in claim 2, wherein a plurality of execution bits are provided in each instruction by said processing step and said skipping step is performed in accordance with a number represented by said plurality of execution bits.